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Application Information

Title Line One:: SEMICONDUCTOR DEVICE, MOUNTING
Title Line Two:: SUBSTRATE AND METHOD OF
Title Line Three:: MANUFACTURING MOUNTING SUBSTRATE,
Title Line Four:: CIRCUIT BOARD, AND ELECTRONIC

Title Line Five::
Total Drawing Sheets::
Docket Number::

INSTRUMENT
12
107281

Continuity Information

>This application is a::
Application One::
Filing Date::
Patent Number::
which is a::
>>Application Two::
Filing Date::
Patent Number::

Prior Foreign Applications

Foreign Application One::	JP 11-039623
Filing Date::	February 18, 1999
Country::	Japan
Priority Claimed::	yes
Foreign Application Two::	
Filing Date::	
Country::	
Priority Claimed::	
Foreign Application Three::	
Filing Date::	
Country::	
Priority Claimed::	